

Docket No. 94100411(EP)USC1X1C1C1 PDDD  
USSN: 09/689,120

PATENT  
Art Unit: 2613

This listing of claims will replace all prior versions, and listings of claims in the application:

**LISTING OF CLAIMS:**

1. (Previously presented) A pipelined video decoder and decompression system for handling a plurality of separately encoded bit streams, said system comprising:

a start code detector responsive to a single serial bit stream for generating control tokens and data tokens, said token including a plurality of data words, each data word having an extension bit which indicates a presence of additional words therein so that said start code detector detects overlapping start codes in said bit stream, a first start code thereby being ignored and a second start code used to create start code tokens;

a token decode circuit interactively associated with said start code detector, said token decode circuit for recognizing certain of said tokens as control tokens pertinent to a respective processing stage and for passing unrecognized control tokens to a succeeding stage; and

a reconfigurable decode and parser processing means responsive to a recognized control token for reconfiguring a particular stage to handle an identified data.

2. (Previously presented) The system as recited in claim 1 further comprising first and second registers, said first register positioned as an input of said decode and parser means and said second register positioned as an output of said decode and parser means.

3. (Previously presented) The system according to claim 1 wherein said

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single serial bit stream of digital bits includes separately encoded pairs of control codes and corresponding data carried therein.

4. (Previously presented) The system according to claim 1 wherein said tokens are altered by said processing stages.

5. - 41. (Cancelled)